

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	PIPELINE-BASED CIRCUIT WITH A POSTPONED CLOCK-GATING MECHANISM FOR REDUCING POWER CONSUMPTION AND RELATED DRIVING METHOD THEREOF				
Application Number : Date : First Named Applicant: Chung-Hui Chen Confirmation Number: Attorney Docket Number: FTCP0017USA					
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Documents being submitted:	Files
us-power-of-attorney-grant	FTCP0017-uspoat.xml us-power-of-attorney-grant.dtd us-power-of-attorney-grant.xsl
us-assignment	FTCP0017-usassn.xml us-assignment.xsl us-assignment.dtd FTCP0017ASS1.tif
us-request	FTCP0017-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	FTCP0017-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	FTCP0017-usdecl.xml us-declaration.dtd us-declaration.xsl
application-body	FTCP0017-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd Ftcp0017usa-01.tif Ftcp0017usa-02.tif Ftcp0017usa-03.tif Ftcp0017usa-04.tif Ftcp0017usa-05.tif

us-declaration	Ftcp0017usa-06.tif
us-declaration	FTCP0017DEC1.tif
us-declaration	FTCP0017DEC2.tif
Comments	